Montage externe

VCC

VCC

R'
10k

R''
10k

PD2'

PD3'

Donnees SERIES
codees MLI

Sorties decodees

GND
F3
F2
F1
F0
+5V
GND
B7
B6
B5
B4
B3
B2
B1
B0
+5V
PD2
PD3
PA0
+5V
GND

VCC=+5V

VCC=+5V

R8
10k

IC1
MC68HC11F1

PF-0 58
PF-2 56
PF-4 54
PF-3 55
PF-5 53
PF-7 51
PB-1 49
PB-4 46
PB-2 48
PB-0 50
PF-1 57
PF-6 52
PB-3 47
PG-4 23
PG-5 22
PG-3 24
PB-5 45
PB-6 44
PG-7 20
PG-6 21
R/W 5
RST 17
PB-7 43
PC-3 12
PC-4 13
PC-1 10
PC-2 11
PC-0 9
PC-6 15
PC-5 14
PC-7 16
PE-0 59
PE-2 63
PE-1 61
PD-3 31

VCC 34
MODA 3
PG-0 27
IRQ 19
PA-0 42
PA-6 36
PD-5 33
PA-4 38
PA-7 35
PA-2 40
PA-5 37
PA-1 41
PA-3 39
RXD/PD-0 28
TXD/PD-1 29
XTAL 7
EXTAL 6
PG-1 26
PE-3 65
XIRQ 18
PE-7 66
PG-2 25
PD-4 32
PB-1 31
PD-2 30
VRH 66
VRL 67
MODB 2
MODA 1

+ C1
10uF

C2
100nF

VCC=+5V

VCC=+5V

R2
1k

C5
47uF

RESET

R3
10M

C3
22pF

X
12 MHz

C4
22pF

Selection mode
STABLE ou IMPULSIONNEL

DIP2
×4

IMPUL=0

STABLE=1

5 4 C3
6 3 C2
7 2 C1
8 1 C0
9 8 G7
10 7 G6
11 6 G5
12 5 G4
13 4 G3
14 3 G2
15 2 G1
16 1 G0

DIP1
×8

VCC=+5V

R7
10k

T2
BC328

R4
1k

VCC=+5V

R6
10k

T1
BC328

R5
1k

VCC

Reseau 4x10K
RES2

VCC

Reseau 8x10k
RES1

R1
4,7k

VCC=+5V

Decodeur serie -> // pour MM53200 & UM3750A		
Size A	Document Number b:decodeur.sch	REV
Date: August 2, 2000	Sheet of	